

Electronic Acknowledgement Receipt

EFS ID:	3545682
Application Number:	10774990
International Application Number:	
Confirmation Number:	9588
Title of Invention:	Static timing model for combinatorial gates having clock signal input
First Named Inventor/Applicant Name:	Matthew J. Amatangelo
Customer Number:	66083
Filer:	Robert M Tuttle
Filer Authorized By:	
Attorney Docket Number:	188082/US
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Application Type:	Utility under 35 USC 111(a)

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File Listing:

Document Number	Document Description	File Name	File Size(Bytes) /Message Digest	Multi Part /.zip	Pages (if appl.)
1	Amendment - After Non-Final Rejection	Amendment_and_Response.pdf	334600 f8afe185cc8f6473aeb51af88d5a2e808a14d3f3	no	8

Warnings:

Information:

2	Miscellaneous Incoming Letter	Transmittal_and_Petition.pdf	334709	no	2
			49f9732c120695560baec67439a21f2d4b23f968		
Warnings:					
Information:					
Total Files Size (in bytes):				669309	
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